Tele-exploitation by the World Wide Web of the MPC parallel machine
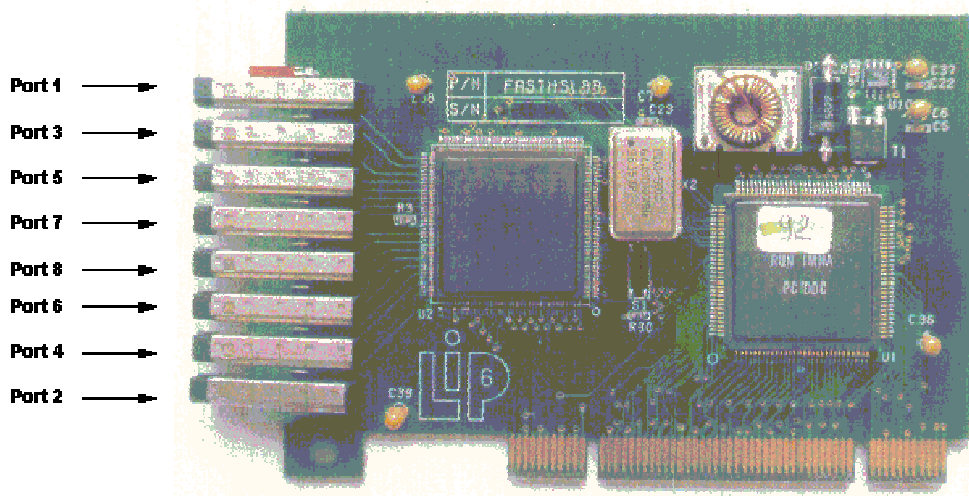
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This demonstration is a part of the global research project called Multi-PC (MPC) which started in 1995. The project goal is to design a high performance/low cost multiprocessor, using standard Pentium based PC-boards as nodes, Unix FreeBSD operating system, and a high speed communication network built on the gigabit-HSL (High Speed Link) IEEE-1355 technology.

We wish to demonstrate here how to use the Job Management System for the MPC parallel machine. This software was designed in the goal to have a user-friendly parallel computer MPC and a system of queues to launch parallel jobs of several users. Two interfaces were realised : a command-line interface and a WWW interface.

The MPC parallel computer of the LIP6 laboratory is formed of height Pentium II-350MHz processors. We use in this demonstration the WWW interface. We also want to demonstrate the hardware components of the project and in particular the new Fast-HSL PCI card used in our MPC computer.

The Fast-HSL PCI card used in the MPC parallel machine.